

The disclosures of these co-pending applications are incorporated herein by cross-reference -

**Page 2, lines 7-13 to be rewritten as follows:**

09/609,139, 09/608,970, 09/609,039, 09/607,852, 09/607,656, 09/609,132,  
09/609,303, 09/610,095, 09/609,596, 09/607,843, 09/607,605, 09/608,178,  
09/609,553, 09/609,233, 09/609,149, 09/608,022, 09/609,232, 09/607,844,  
09/607,657, 09/608,920, 09/607,985, 09/607,990 09/607,196, 09/606,999

The disclosures of these co-pending applications are incorporated herein by cross-reference -

**Page 2, lines 18-33 to be rewritten as follows:**

--09/575,197, 09/575,195, 09/575,159, 09/575,132, 09/575,123, 09/575,148,  
09/575,130, 09/575,165, 09/575,153, 09/575,118, 09/575,131, 09/575,116,  
09/575,144, 09/575,139, 09/575,186, 09/575,185, 09/575,191, 09/575,145,  
09/575,192, 09/575,181, 09/575,193, 09/575,156, 09/575,183, 09/575,160,  
09/575,150, 09/575,169, 09/575,184, 09/575,128, 09/575,180, 09/575,149,  
09/575,179, 09/575,187, 09/575,155, 09/575,133, 09/575,143, 09/575,196,  
09/575,198, 09/575,178 09/575,164, 09/575,146, 09/575,174, 09/575,163,  
09/575,168, 09/575,154, 09/575,129, 09/575,124, 09/575,188, 09/575,189,  
09/575,162, 09/575,172, 09/575,170, 09/575,171, 09/575,161, 09/575,141,  
09/575,125, 09/575,142, 09/575,140, 09/575,190, 09/575,138, 09/575,126,  
09/575,127, 09/575,158, 09/575,117, 09/575,147, 09/575,152, 09/575,176,  
09/575,115, 09/575,114, 09/575,113, 09/575,112, 09/575,111, 09/575,108,  
09/575,109, 09/575,110

The disclosures of these co-pending applications are incorporated herein by cross-reference.-

**Please replace the paragraph beginning at page 9, line 6, with the following rewritten paragraph:**

--The viewer 100 is a netpage system enabled device and communicates with a netpage system in a similar manner to that disclosed in the co-pending applications referred to earlier and in particular to applications USSN 09/722,142 and USSN 09/721,893.--

**Please replace the paragraph beginning at page 13, line 20, with the following rewritten paragraph:**

--The sensor device 112 is infrared sensitive. The CCD 162 is sensitive to infrared light, either inherently or by use of filters and the LED 160 emits infrared light, again inherently or by use of filters. The lens 168 is focused on the plane of the inner and outer feet 182 and 184, as this is where a substrate to be sensed will be located. The sensor device is capable of detecting infrared absorptive tags, such as netpage tags. For a full description of the processes involved, reference is made to our co-pending application USSN

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(cont)

09/721,893 referred to earlier. The CCD 162, the LED 160 and processing functions incorporated in the processor chip 170 are similar to those disclosed in the co-pending application.--

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**Please replace the paragraph beginning at page 14, line 17, with the following rewritten paragraph:**

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--The print assembly 200 is preferably an inkjet type printer and more preferably a full-color inkjet. Accordingly the ink cartridge 204 includes multiple inks. In the preferred embodiment the print assembly is a full-color CMY or CMYK printer and the ink cartridge 204 includes three or four separate ink chambers 252. Whilst any inkjet printhead may be used, more preferably the printhead is a microelectromechanical system (MEMS) type printhead (Memjet), such as that disclosed in our co-pending application USSN 09/575,141.-

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**Please replace the paragraphs beginning at page 15, line 13, with the following rewritten paragraphs:**

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--The operation of the print assembly 202 is controlled by a dedicated print engine/controller (PEC) chip 281 located on the PCB 118. An example of a suitable PEC is described in our co-pending application USSN 09/575,108. The PEC chip 281 generates bi-level dot data for the printhead in real time and otherwise controls the operation of the printhead. Communication with the printhead assembly 202 is via a flexible PCB 228 which engages the PCB 118 via connector 230. The DC motor 286 is connected to the PCB via a flexible PCB 231.

A master QA chip 282, for example as described in our co-pending application USSN 09/113,223, is provided which the PEC uses to authenticate an identical QA chip 283 embedded in the replaceable color ink cartridge 204. A raster image processor (RIP) DSP 284 may be used for rendering print data at high speed. Depending on the desired print quality and speed and the performance of the processor, the RIP DSP may be omitted with the processor performing rasterization. Depending on the need to accurately control ink quality and to monitor consumption, and on the availability of alternate mechanisms for detecting ink depletion, the inclusion of the QA chips may not be needed.--

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